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**Amendments to the specification:**

Please amend page 8, last paragraph to page 9, second paragraph as follows:

Then, the valid data calculating module 42 determines whether the amount of valid packet data in the buffering memory unit 41 exceeds the preset main memory unit bus requesting threshold value or not. If yes, then the main memory unit bus requesting module 44 determines whether the data length exceeds a per-set size for storing packets of the main memory unit or not. If yes, the main memory unit bus request is no longer sent until the data transfer is over. This situation usually occurs when there is an abnormal data packet or when the size of main memory for storing packet data is too small. If no, the main memory unit bus requesting module 44 sends a main memory unit bus 20 usage request. The amount of the valid packet data is the number of bytes between the write-in index 101 and the read-out index 102. The main memory bus requesting threshold is set to 18 data bytes in the present invention, and the length ~~of~~ valid data bytes in the buffer is set to 20 bytes, so the memory bus requesting module 44 sends a main memory unit bus 20 usage request.

Moreover, the data length calculating module 43 determines if a sending data byte is the end of the data; if yes, a data end index 103 is written into the register, which allows the network communication system connecting module 40 makes sure that the end point of the data packets in order to write into the buffering memory unit 41. Through the addresses of the data end index 103 and the read-out index ~~104~~102, the length of the current data packet byte. In this embodiment, this is the amount of data bytes of the data packet 60. Please refer to FIG. 2B, if the end of the sending data packet 60' is not yet written into the buffering memory unit 41 within the buffer, then the data end index 103 address is set to infinitely faraway.

Please amend page 10, second to last paragraph as follows:

In step S208, the main memory unit bus requesting module 44 determines whether the packet length exceeds the preset size of packet storage in the main memory unit or not. If yes~~no~~, then perform step S209; if ~~no~~yes, perform step S216.